

The diagram is a detailed hand-drawn schematic of a digital circuit, likely a microcontroller system. It features several key components and their interconnections:

- Address and Data Buses:**
 - Q1 (AB0-AB 15):** Address bus lines.
 - Q2 (I/WR):** Input/Write signal line.
 - Q8 (I/MREQ):** Memory Request signal line.
 - Q10 (DB0-DB7):** Data bus lines.
 - Q4 (I/RD-11):** Read signal line.
- Control Signals:**
 - X1: A25 (RFSH):** Refresh signal.
 - X2: B09 (MEMDI), A21 (MEMDI1), B21 (MEMDI2):** Memory Data Input signals.
- Components:**
 - PROM Y708:** Programmable Read-Only Memory chips (A26, A27, A28).
 - RAM X2A4:** Random Access Memory chips (A18, A19, A20, A21, A22, A23, A24, A25).
 - Logic Gates:** DC U205 (A3, A4), U216 (A2, A1).
 - Resistor R1:** Connected to the RFSH signal.
- Connections and Annotations:**
 - Handwritten Notes:** "alles 0 für Auswahl unter Decoder" (all 0 for selection under decoder), "keine Auswahl bei MEMDI" (no selection at MEMDI), "keine Auswahl bei RFSH" (no selection at RFSH), "Datenbus trennen" (separate data bus).
 - Pin Connections:** Detailed pin numbers (e.g., 01, 02, 03, 04, 05, 06, 07, 08, 09, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34) are shown for many components.
 - Signal Paths:** Lines connect components to the address, data, and control buses, often labeled with bus names like DB0, DB1, DB2, DB3, DB4, DB5, DB6, DB7.

8620/14 21-235 VY Freiberg Ag 307 7A Bl 15.4 137