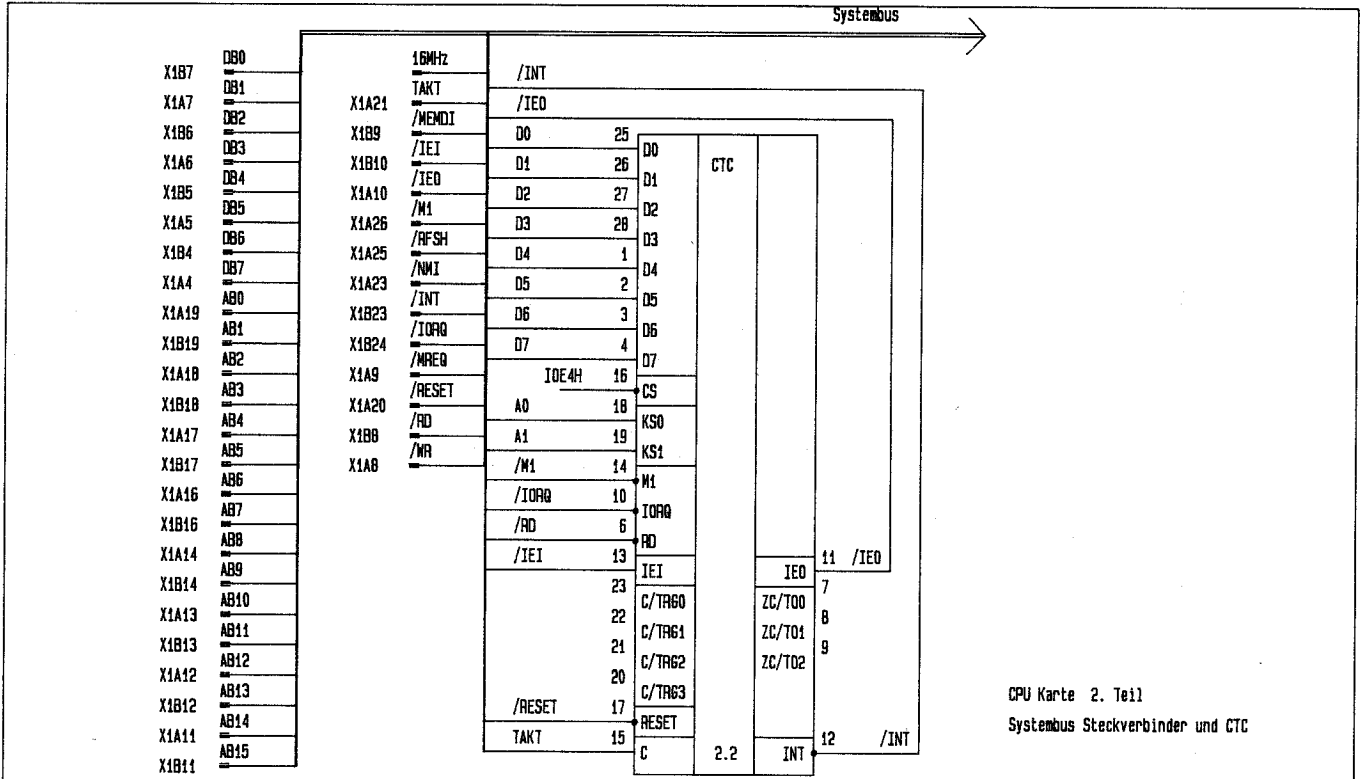


CPU, Speicheransteuerung, RAM-Floppy

FILE CPUJTZ.BLD

Schritt 6
 Lot 1
 Kette 50



CPU Karte 2. Teil
 Systembus Steckverbinder und CTC

