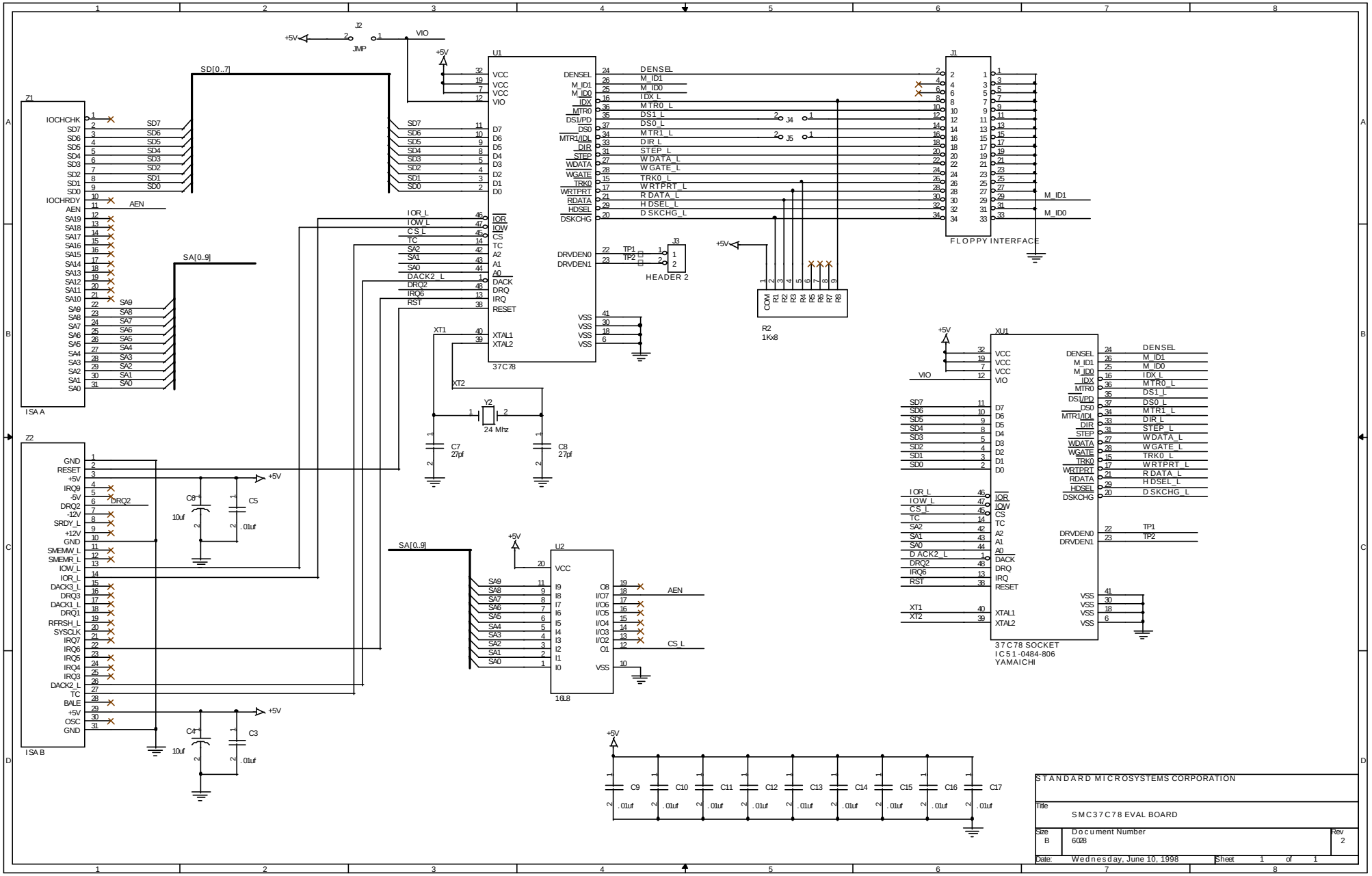
J2
JMP
+5V
VIO
U1
37C78
Z1
ISA A
Z2
ISA B
J1
FLOPPY INTERFACE
J3
HEADER 2
J4
J5
R2
1Kx8
Y2
24 Mhz
C7
27pf
C8
27pf
C5
.01uf
C6
10uf
C3
.01uf
C4
10uf
U2
16L8
XU1
37C78 SOCKET
IC51-0484-806
YAMAICHI
C9 C10 C11 C12 C13 C14 C15 C16 C17
.01uf
STANDARD MICROSYSTEMS CORPORATION
Title
SMC37C78 EVAL BOARD
Size
B
Document Number
6028
Rev
2
Date:
Wednesday, June 10, 1998
Sheet 1 of 1